

WHAT IS CLAIMED IS:

1. A circuit card package, comprising:

a first circuit card having a first connector mounted thereon;

5 an interface adaptor board secured to said first connector and oriented in a plane orthogonal to said first circuit card; and

at least one second connector mounted to said interface adaptor board and adapted to have secured thereto a respective one of at least one second circuit card oriented parallel to said first circuit card.

2. The package according to Claim 1 further comprising:

15 a substantially planar faceplate bracket secured orthogonally to said first circuit card, said faceplate bracket being formed with at least one opening each for receiving a respective one of said at least one second circuit card therethrough in a direction parallel to said first circuit card; and

20 a second bracket member secured to said first circuit card and to said faceplate bracket, said second bracket member being formed from planar sheet material and being bent at a right angle so as to have a first portion orthogonal to said first circuit card and a second portion orthogonal to said faceplate bracket;

25 wherein said interface adaptor board is secured to said first portion of said second bracket member.

3. The package according to Claim 2 further comprising:

30 at least one latch mechanism secured to said first portion of said second bracket member and each cooperating with structure on a respective one of said at least one second circuit card to selectively retain said respective second circuit card in engagement with a

respective one of said at least one second connector.

5        4.    The package according to Claim 3 wherein each of said at least one second circuit card is formed with a respective opening therethrough and each of said at least one latch mechanism includes a spring biased pin adapted to extend through said respective card opening to prevent movement of said respective second circuit card.

10       5.    The package according to Claim 1 further comprising:  
         structure for preventing hot insertion of said at least one second circuit card into said package.